



Digital Electronics

S Salivahanan, S Arivazhagan

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Enhanced use of digital circuits in all disciplines of engineering has created an urge among students for in-depth knowledge. A single textbook dealing with the basics of digital technology, including the design aspects of circuits, is the need of the hour. We present this revised edition to fulfill the requirements of the students of various BE/BTech degree courses including Electronics and Communication Engineering, Electrical and Electronics Engineering, Information Technology, Computer Science and Engineering and Electronics and Instrumentation Engineering offered in all Indian universities. The book will also serve as textbook to students of BSc and MSc courses in Electronics and Communication, Information Technology, Computer Science, Applied Physics and Computer Software, MCA, AMIE, graduate IETE and Diploma courses and as reference for competitive examinations. This book is divided into 13 chapters. Each chapter begins with an introduction and ends with review questions and problems. Chapter 1 introduces the number system, binary arithmetic and codes. Chapter 2 deals with Boolean algebra, simplification using Boolean theorems, K-map method and Quine-McCluskey method. In Chapter 3, logic gates and implementation of switching functions using basic and universal gates are discussed. Chapters 4 and 5 give a brief description on combinational circuits like arithmetic and data processing. Chapter 6 describes flip-flops and realization using flip-flops. Chapter 7 discusses synchronous and asynchronous counters and the design of synchronous counters in detail. Chapter 8 presents shift registers, shift counters and ring counters and their design. Chapters 9 and 10 are devoted to the design of synchronous and asynchronous sequential circuits, respectively. Chapter 11 concentrates on memory devices, which includes ROM, RAM, PLA, PAL and FPGA. Chapter 12 deals with logic families like TTL and CMOS logic circuits. Chapter 13 describes Hardware Description Language (HDL) for Digital Circuits. A number of appendices provide the student a quick glance of the topics. All the topics have been illustrated with clear diagrams. A variety of examples are given to enable students to design digital circuits efficiently. We sincerely thank the managements of SSN College of Engineering, Chennai and Mepco Schlenk Engineering College, Sivakasi, for their constant encouragement and providing necessary facilities for completing this project. Our colleagues Dr V S Kanchana Bhaaskaran, Mr R Rengaraj and Mr V R Venkatasubramani have reviewed the additional materials. Our thanks are due to Mr R Gopalakrishnan for word—processing the additional script.

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